

Fig. 1

The block diagram illustrates a cardiac rhythm management system 40. It features a central microprocessor (μP) 41, which is bidirectionally connected to a MEMORY 44 and a Therapy Deliver 51. The μP 41 is also connected to an A/D converter 42, a QRS Detector 43, and a Telemetering Xcut 43a. The A/D converter 42 is connected to the QRS Detector 43. The QRS Detector 43 is connected to the Telemetering Xcut 43a. The Telemetering Xcut 43a is connected to a GAIN ADJ. 46. The GAIN ADJ. 46 is connected to a switch 45. The switch 45 is connected to the A/D converter 42. The switch 45 is also connected to two input lines, E_1 and E_2 . The system is powered by a Batt. 49 and a CLK 50. An Addtl. Sensors 48 are connected to the μP 41. A dashed line 47 indicates a connection between the MEMORY 44 and the Therapy Deliver 51.

FIG. 2

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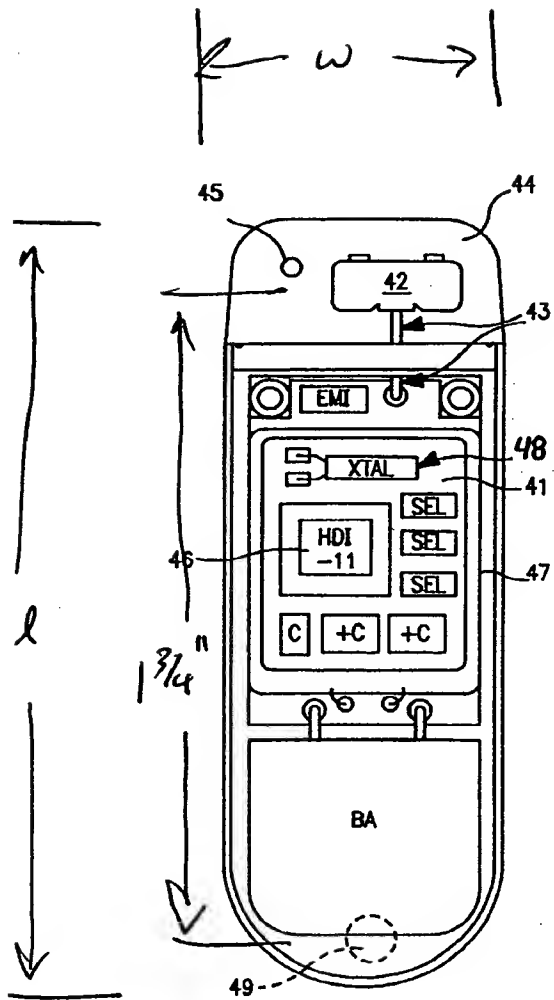


FIG. 2A

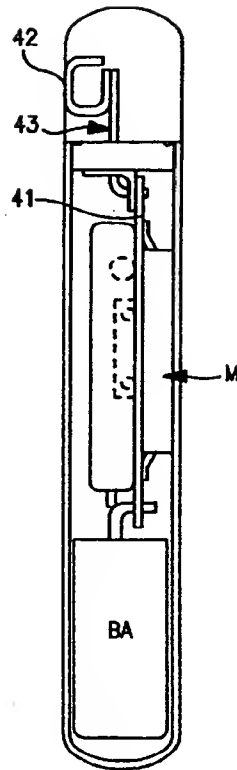


FIG. 2B

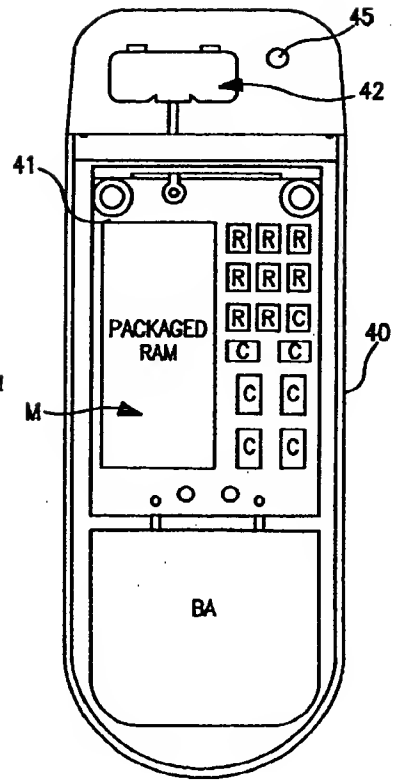


FIG. 2C

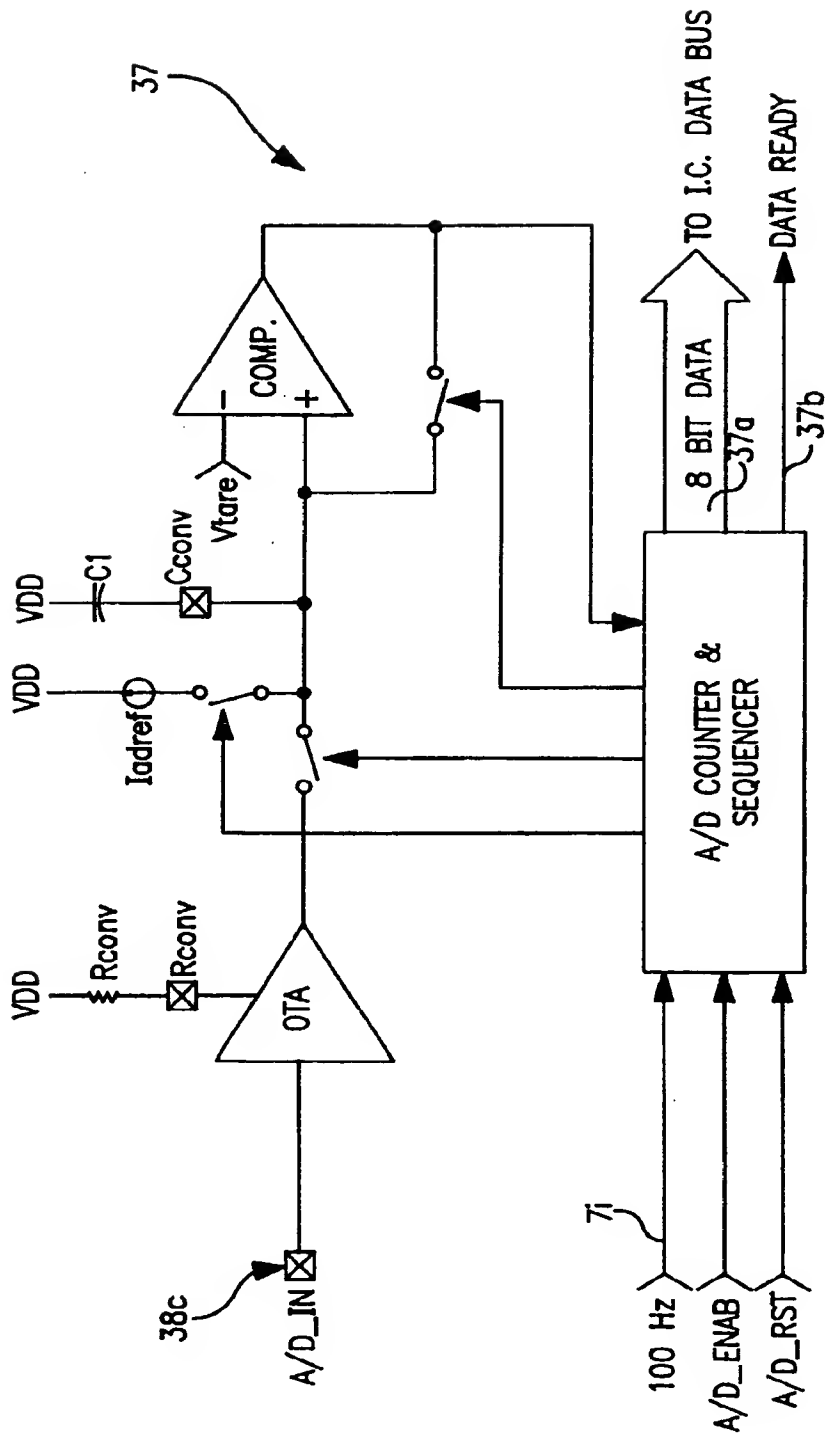


FIG. 3A

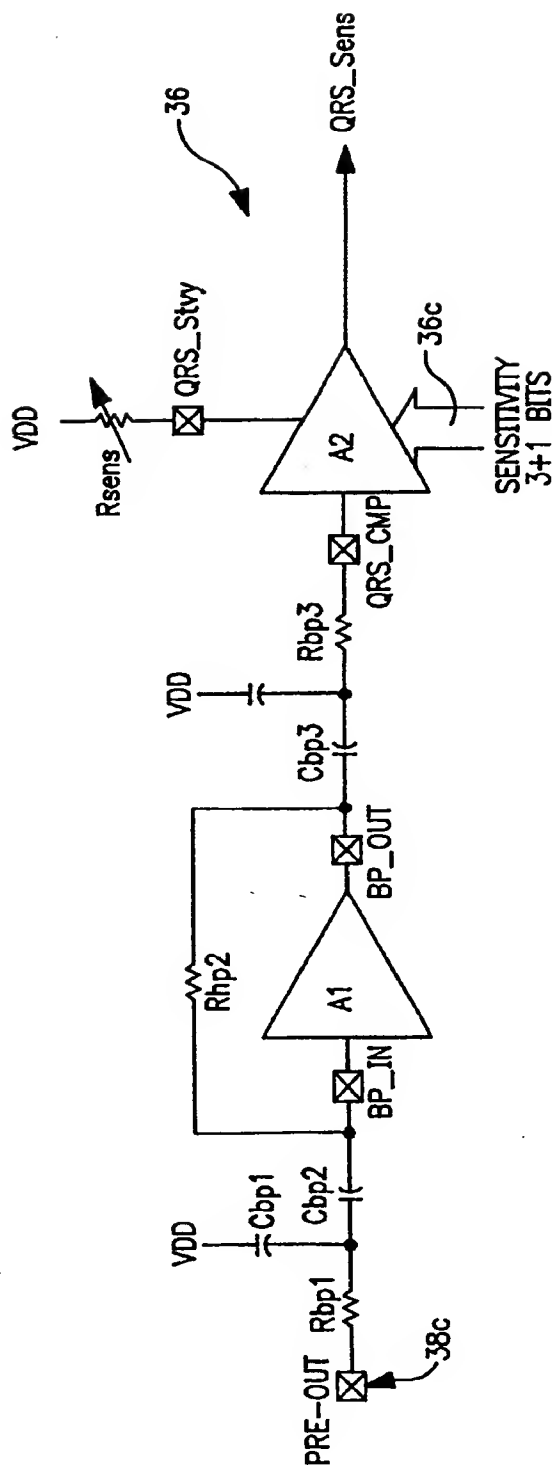


FIG. 3C

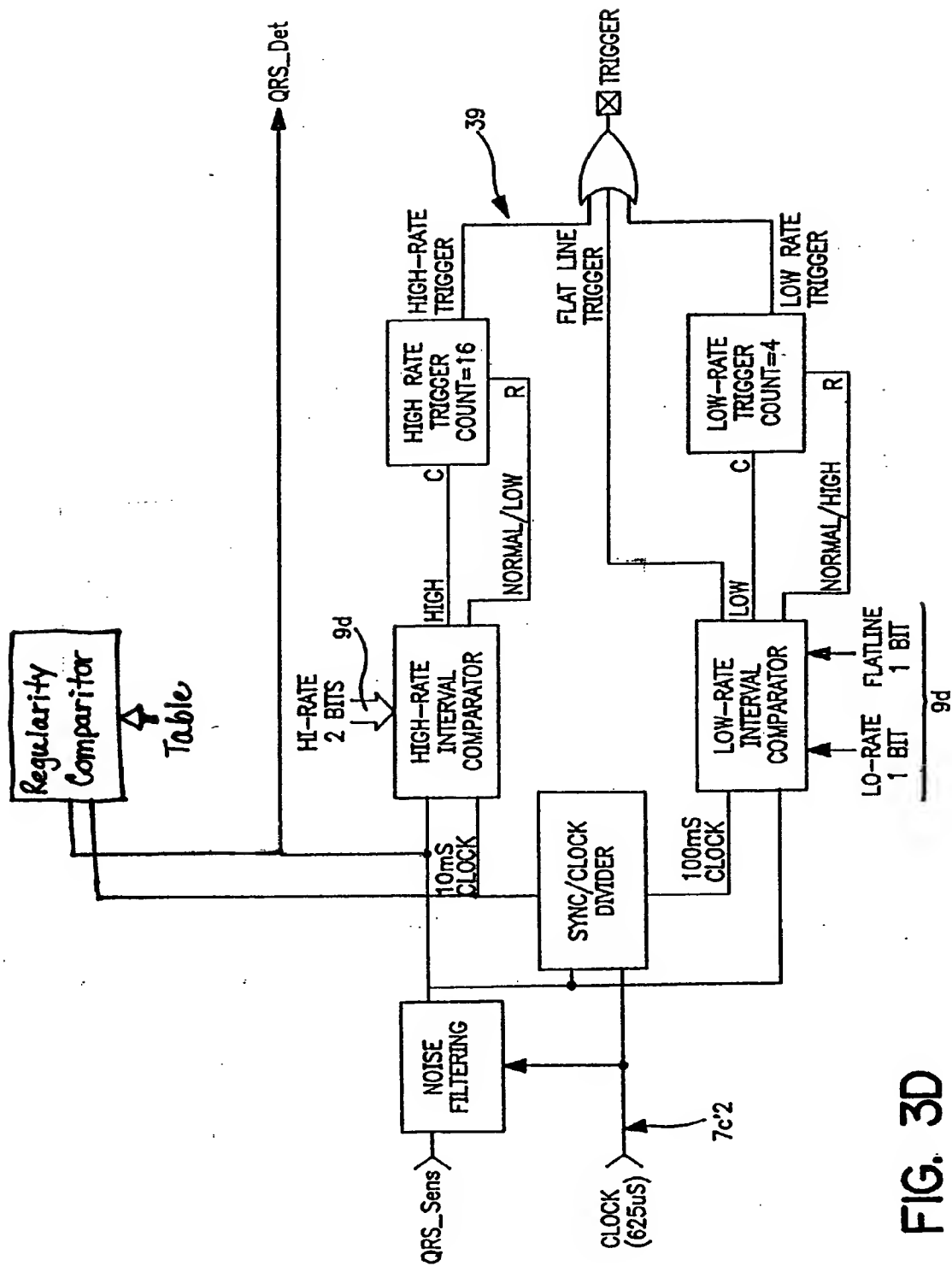


FIG. 3D

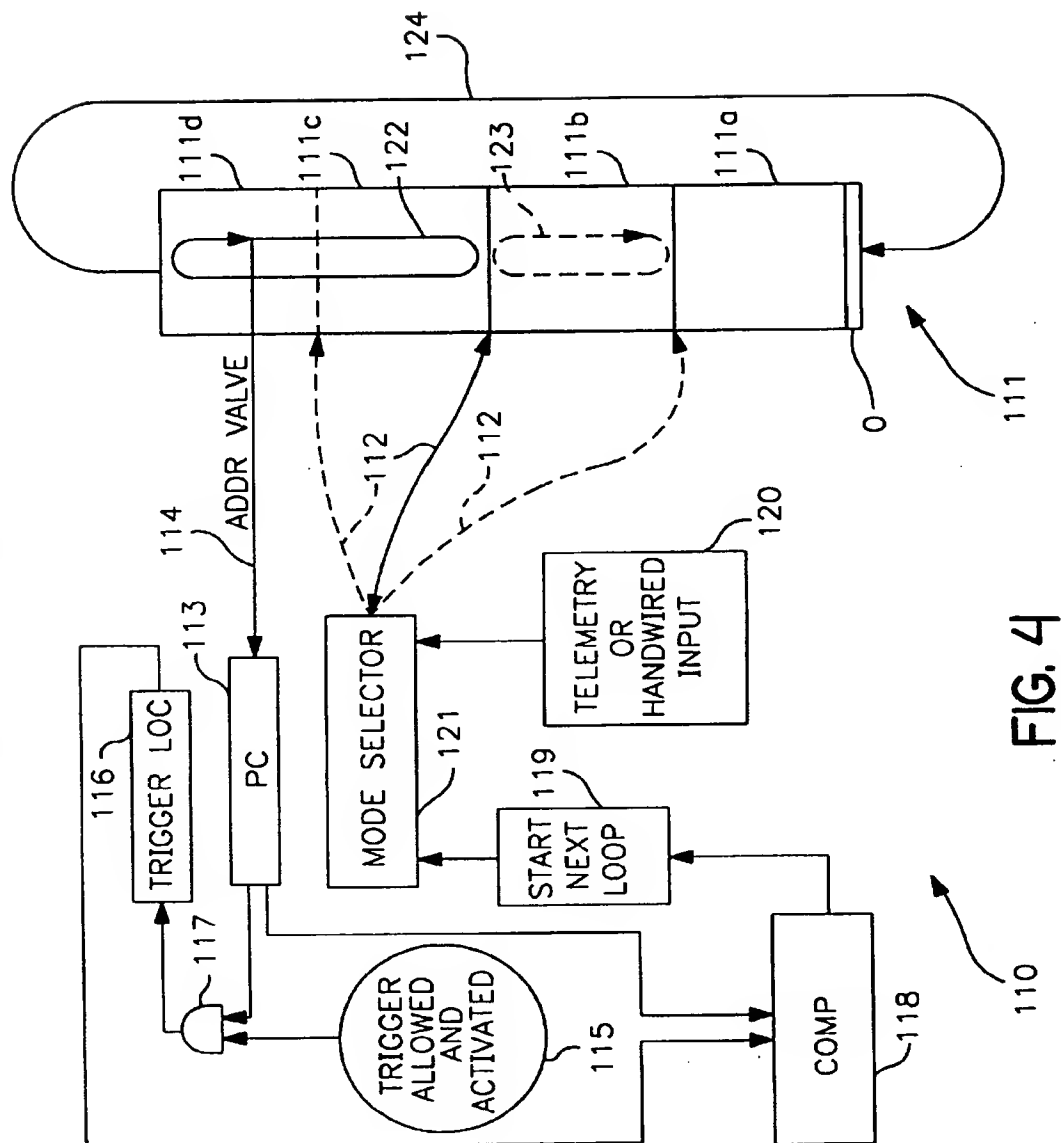


FIG. 4

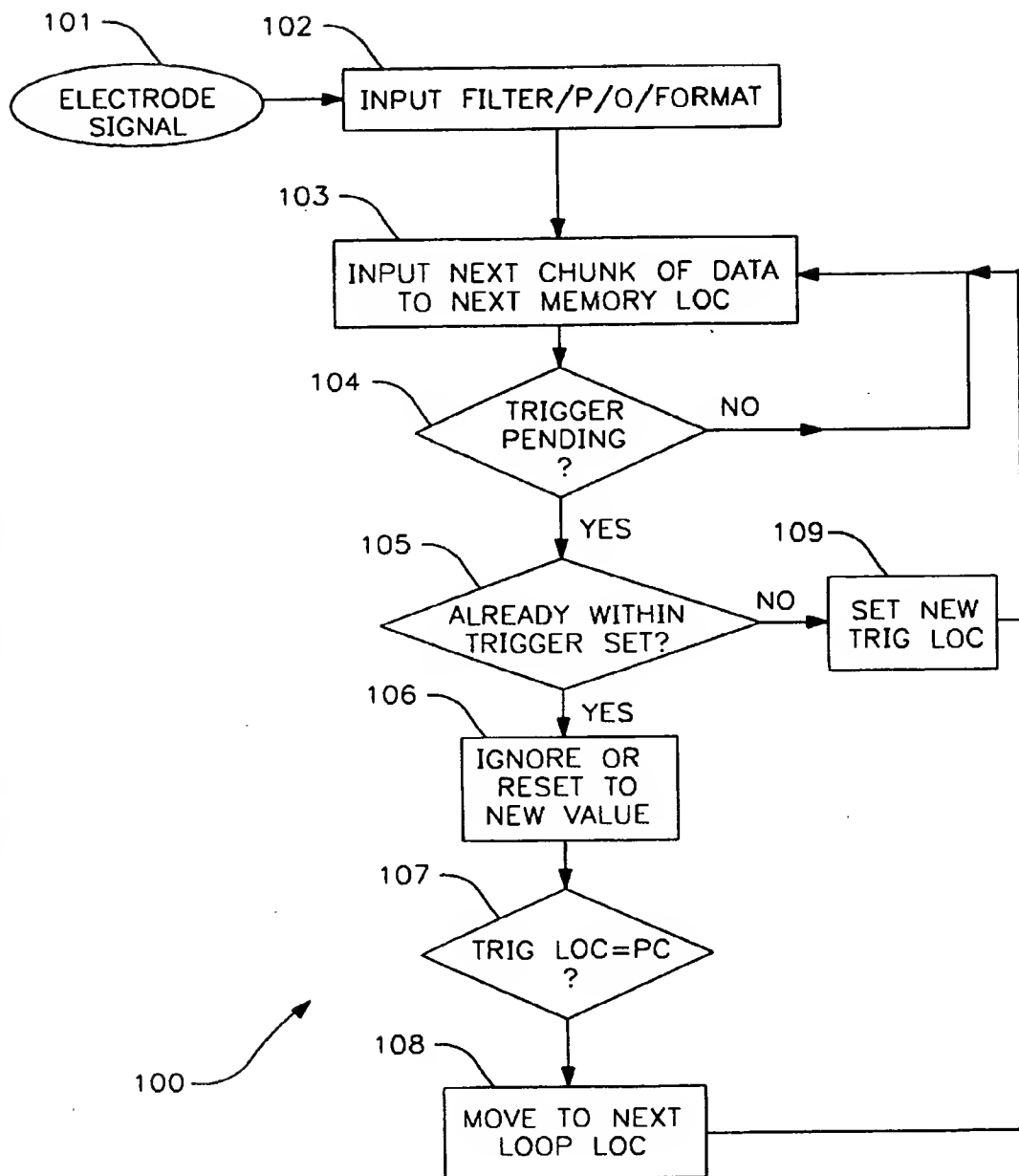


FIG. 5

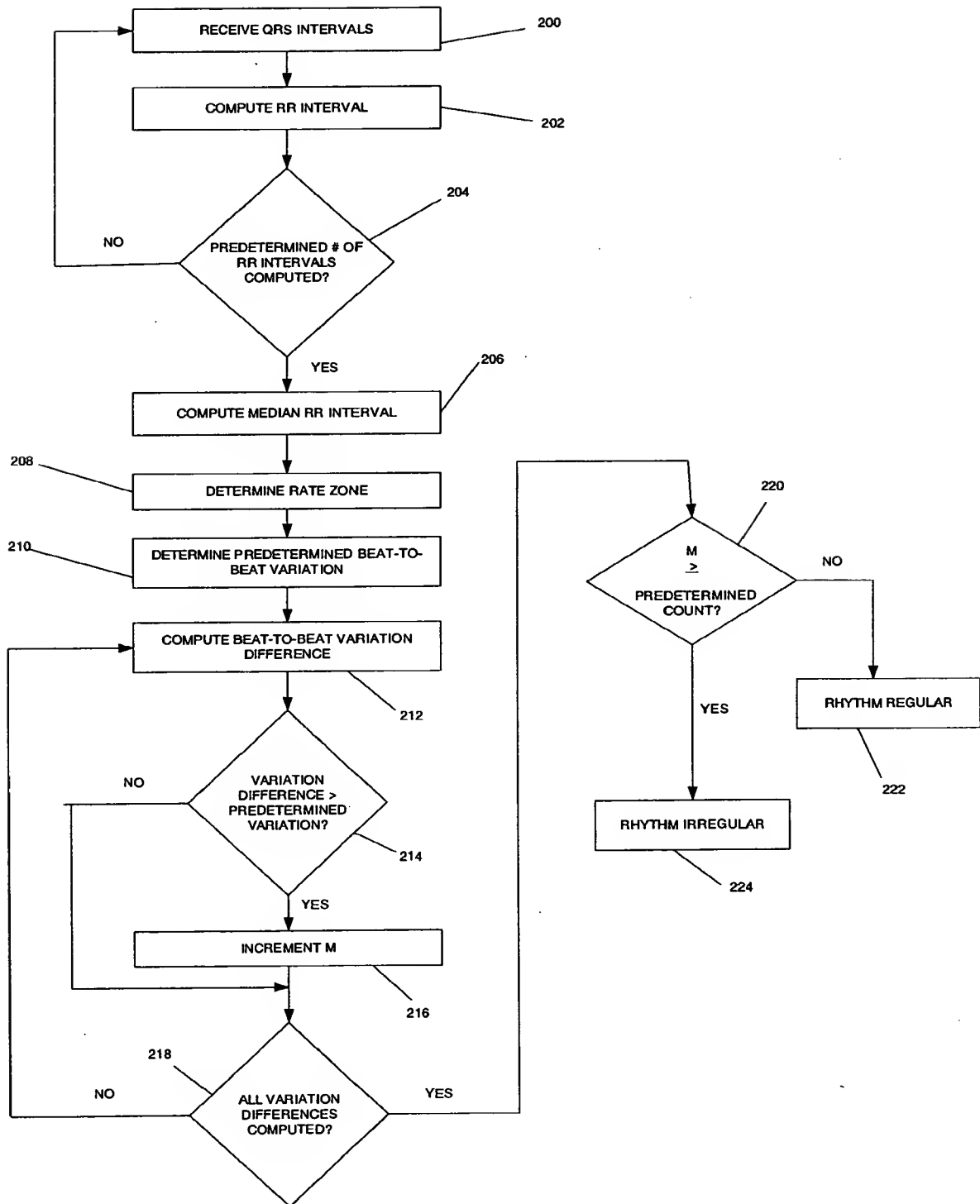


Fig. 6

Fig.7

Rate	Δ RR	N
> 500 ms	50 ms	8
(400 ms, 500 ms]	25 ms	8
(320 ms, 400 ms]	15 ms	5
< 320 ms	15 ms	5